

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: Katsuto TANAHASHI et al.

Art Unit: 2815

Application Number: 10/743,793

Examiner: Jay C. Kim

Filed: December 24, 2003

Confirmation Number: 9788

Semiconductor substrate and manufacturing method thereof For:

Attorney Docket Number:

032206

Customer Number:

38834

## **PETITION FOR EXTENSION OF TIME**

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

July 18, 2008

Sir:

Applicants petition the Commissioner for Patents to extend the time for response to the Office Action dated March 18, 2008 for 1 month from June 18, 2008 to July 18, 2008.

Attached please find a check in the amount of \$120.00 for the cost of the extension of time. If any additional fees are due in connection with this paper, please charge Deposit Account No. 50-2866.

Respectfully submitted,

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